



JFW AF/2822

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application

Jack Hwang et al. § Art Unit: 2822  
Serial No.: 10/644,417 § Examiner: Mark V. Prenty  
Filed: August 20, 2003 §  
For: Forming Strained Source Drain § Docket: ITL.0764D1US  
Junction Field Effect Transistors § P14416D  
§

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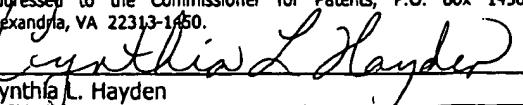
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Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**REPLY TO FINAL REJECTION**

In response to the final rejection mailed August 26, 2004, please amend the above-referenced patent application as follows:

Date of Deposit: September 17, 2004
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.
 Cynthia L. Hayden